

N13T1, 2N6027, 2N6028

Preferred Device

Programmable Unijunction Transistor

Programmable Unijunction Transistor Triggers

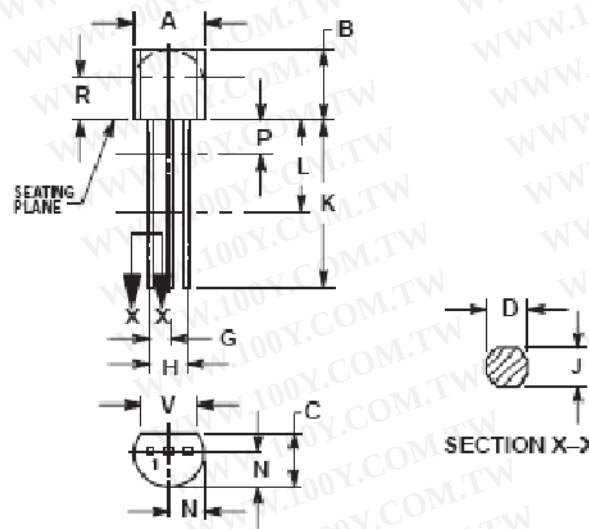
- Programmable — R_{BB} , η , I_V and I_P
- Low On-State Voltage — 1.5 Volts Maximum @ $I_P = 50$ mA
- Low Gate to Anode Leakage Current — 10 nA Maximum
- High Peak Output Voltage — 11 Volts Typical
- Low Offset Voltage — 0.35 Volt Typical ($R_G = 10$ k ohms)
- Device Marking: Logo, Device Type, e.g., 2N6027, Date Code

PUTs
 40 VOLTS
 300 mW



TO-92 (TO-226AA)
 CASE 029
 STYLE 16

PIN ASSIGNMENT		
1		Anode
2		Gate
3		Cathode



PACKAGE DIMENSIONS

TO-92 (TO-226AA)
 CASE 029-11
 ISSUE AJ

- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
 4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.175	0.205	4.45	5.20
B	0.170	0.210	4.32	5.33
C	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
H	0.095	0.105	2.42	2.66
J	0.015	0.020	0.38	0.50
K	0.500	—	12.70	—
L	0.250	—	6.35	—
N	0.080	0.105	2.04	2.66
P	—	0.100	—	2.54
R	0.115	—	2.93	—
V	0.135	—	3.43	—

STYLE 16:
 PIN 1. ANODE
 2. GATE
 3. CATHODE